



Global LCD Panel Exchange Center

T460HW08 V7 Product Specification

Model Name: T460HW08 V7

Issue Date: 2011/1/12

)Preliminary Specifications (*)Final Specifications

Customer Signature	Date	AUO	Date							
Approved By		Approval By PM Director Yen Ting Chiu Yeu Ting Chiu 2010/1/15								
Note		Reviewed By RD Director Eugene CC Chen Gugene Chen Reviewed By Project Lea Max Chen Max Chen 1.10	2010							
		Prepared By PM Viola Lu								

design, other customers' design, and customer consigned components.

^{*} This model fails to pass AUO THB and HTO/LTO tests due to egg mura and fail to pass AUO HTS test due to curtain mura. Also, this model fails to pass vibration, shock, packing vibration, and packing drop tests. Therefore, AUO cannot guarantee backlight design and customers' consigned components.

^{*} Due to lack of completed RA data, AUO cannot guarantee product issues related to backlight





Contents

No		
		CONTENTS
		RECORD OF REVISIONS
1		GENERAL DESCRIPTION
2		ABSOLUTE MAXIMUM RATINGS
3		ELECTRICAL SPECIFICATION
	3-1	ELECTRIACL CHARACTERISTICS
	3-2	INTERFACE CONNECTIONS
	3-3	SIGNAL TIMING SPECIFICATION
	3-4	SIGNAL TIMING WAVEFORM
	3-5	COLOR INPUT DATA REFERENCE
	3-6	POWER SEQUENCE
	3-7	BACKLIGHT SPECIFICATION
4		OPTICAL SPECIFICATION
5		MECHANICAL CHARACTERISTICS
6		RELIABILITY TEST ITEMS
7		INTERNATIONAL STANDARD
	7-1	SAFETY
	7-2	EMC
8		PACKING
	8-1	DEFINITION OF LABEL
	8-2	PACKING METHODS
	8-3	PALLET AND SHIPMENT INFORMATION
9		PRECAUTION
	9-1	MOUNTING PRECAUTIONS
	9-2	OPERATING PRECAUTIONS
	9-3	ELECTROSTATIC DISCHARGE CONTROL
	9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE
	9-5	STORAGE
	9-6	HANDLING PRECAUTIONS FOR PROTECT FILM





Record of Revision

Version	Date	Page	Description
0.0	2011/1/12		First release
0.1	2011/1/25	17	Update Optical Specification
<u> </u>			





1. General Description

This specification applies to the 46.0 inch Color TFT-LCD Module T460HW08 V7. This LCD module has a TFT active matrix type liquid crystal panel 1,920x1,080 pixels, and diagonal size of 46.0 inch. This module supports 1,920x1,080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot.

The T460HW08 V7 has been designed to apply the 10-bit 4 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

* General Information

Items	Specification	Unit	Note
Active Screen Size	46.00	inch	♦
Display Area	1018.08(H) x 572.67(V)	mm	
Outline Dimension	1049(H) x 604.6(V) x 24.7(D)	mm	Refer to the customer's design
Driver Element	a-Si TFT active matrix		
Bezel Opening	1026(H) x 580.6(V)	mm	Refer to the customer's design
Display Colors	10 bit, 1073.7M	Colors	
Number of Pixels	1,920x1,080	Pixel	
Pixel Pitch	0.53025 (H) x 0.53025 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	S/C		
Rotate Function	Achievable		Note 1

Note 1: Rotate Function refers to LCD display could be able to rotate.



Global LCD Panel Exchange Center

T460HW08 V7 Product Specification

2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

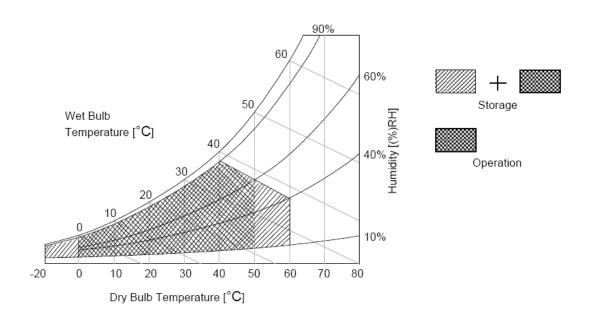
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39[°]C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.

Note 3: Surface temperature is measured at 50°C Dry condition







3. Electrical Specification

The T460HW08 V7 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other is to power back light unit.

3.1 Electrical Characteristics

3.1.1: DC Characteristics

	Doromotor	Cumbal		Value		Lloit	Note	
	Parameter	Symbol	Min.	Тур. Мах		Unit	TVOIC	
LCD								
Power Sup	pply Input Voltage	V_{DD}	10.8	12	13.2	V_{DC}		
Power Sup	pply Input Current	I _{DD}		0.95	1.05	Α	1	
Power Cor	nsumption	Pc		11.4	12.6	Watt	1	
Inrush Cur	rent	I _{RUSH}			2	Α	2	
	Input Differential Voltage	V _{ID}	200	400	600	mV_{DC}	3	
LVDS	Differential Input High Threshold Voltage	V_{TH}	+100		+300	mV_{DC}	3	
Interface	Differential Input Low Threshold Voltage	V _{TL}	-300		-100	mV_{DC}	3	
	Input Common Mode Voltage	V _{ICM}	1.1	1.25	1.4	V_{DC}	3	
CMOS	Input High Threshold Voltage	V _{IH} (High)	2.7	-	3.3	V_{DC}	4	
Interface	Input Low Threshold Voltage	V _{IL} (Low)	0		0.6	V_{DC}	4	

LED lightbar and LED Backlight structure is designed by customer, AUO cannot guarantee life time and backlight power consumption.





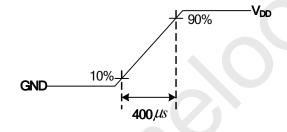
3.1.2: AC Characteristics

Global LCD Panel Exchange Center

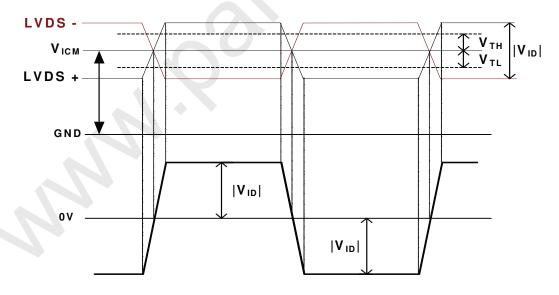
	Parameter	Cymbol		Value		Unit	Note
	Farameter	Symbol	Min.	Тур.	Max	Offic	Note
	Input Channel Pair Skew Margin	t _{SKEW (CP)}	-500		+500	ps	5
LVDS	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%		Fclk +3%	MHz	6
Interface	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30	1	200	KHz	6
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	-0.4 -0.5		0.4 0.5	ns	7

Note:

- 1.
- Measurement condition: Rising time = 400us



3. $V_{ICM} = 1.25V$



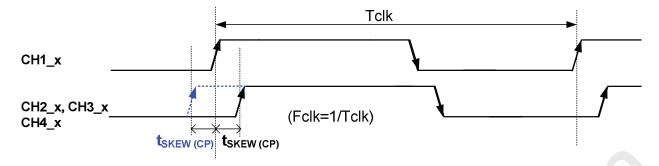
- **4.** The measure points of V_{IH} and V_{IL} are in LCM side after connecting the System Board and LCM.
- 5. Input Channel Pair Skew Margin

© Copyright AUO Optronics Corp. 2009 All Rights Reserved.



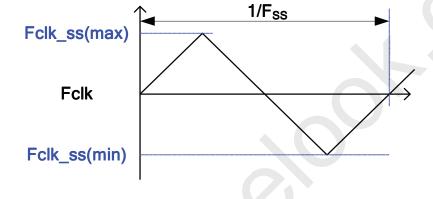
Global LCD Panel Exchange Center

T460HW08 V7 Product Specification Rev.0 1



Note: x = 0, 1, 2, 3, 4

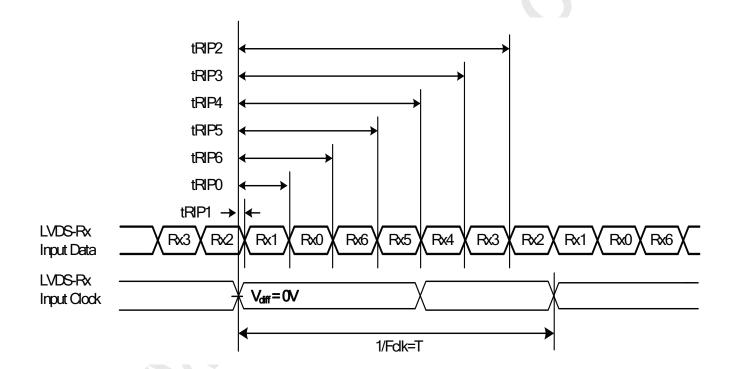
6. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures





7. Receiver Data Input Margin

Parameter	Cymbal		Rating		Unit	Note
Parameter	Symbol	Min	Туре	Max	Unit	Note
Input Clock Frequency	Fclk	Fclk (min)		Fclk (max)	MHz	T=1/Fclk
Input Data Position0	tRIP1	- tRMG	0	tRMG	ns	
Input Data Position1	tRIP0	T/7- tRMG	T/7	T/7+ tRMG	ns	
Input Data Position2	tRIP6	2T/7- tRMG	2T/7	2T/7+ tRMG	ns	
Input Data Position3	tRIP5	3T/7- tRMG	3T/7	3T/7+ tRMG	ns	
Input Data Position4	tRIP4	4T/7- tRMG	4T/7	4T/7+ tRMG	ns	
Input Data Position5	tRIP3	5T/7- tRMG	5T/7	5T/7+ tRMG	ns	
Input Data Position6	tRIP2	6T/7- tRMG	6T/7	6T/7+ tRMG	ns	



- **8.** The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.
- 9. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value.

[Operating condition: Continuous operating at Ta = $25\pm2^{\circ}$ C]





3.2 Interface Connections

LCD connector: FP7S082HA1 (JAE, LVDS connector)

Symbol	Description	PIN	Symbol	Description
V_{DD}	Power Supply, +12V DC Regulated	26	CH3_0+	LVDS Channel 3, Signal 0+
V_{DD}	Power Supply, +12V DC Regulated	27	CH3_1-	LVDS Channel 3, Signal 1-
V_{DD}	Power Supply, +12V DC Regulated	28	CH3_1+	LVDS Channel 3, Signal 1+
V_{DD}	Power Supply, +12V DC Regulated	29	CH3_2-	LVDS Channel 3, Signal 2-
V_{DD}	Power Supply, +12V DC Regulated	30	CH3_2+	LVDS Channel 3, Signal 2+
N.C.	No connection	31	GND	Ground
GND	Ground	32	CH3_CLK-	LVDS Channel 3, Clock -
GND	Ground	33	CH3_CLK+	LVDS Channel 3, Clock +
GND	Ground	34	GND	Ground
CH1_0-	LVDS Channel 1, Signal 0-	35	CH3_3-	LVDS Channel 3, Signal 3-
CH1_0+	LVDS Channel 1, Signal 0+	36	CH3_3+	LVDS Channel 3, Signal 3+
CH1_1-	LVDS Channel 1, Signal 1-	37	CH3_4-	LVDS Channel 3, Signal 4-
CH1_1+	LVDS Channel 1, Signal 1+	38	CH3_4+	LVDS Channel 3, Signal 4+
CH1_2-	LVDS Channel 1, Signal 2-	39	GND	Ground
CH1_2+	LVDS Channel 1, Signal 2+	40 <	SCL	EEPROM Serial Clock
				3D function control
GND	Ground	41	3D_EN	High(3.3V) : 3D Enable
				Open/Low(GND) : 3D Disable
CH1_CLK-	LVDS Channel 1, Clock -	42	N.C	No connection
		\		TCON I2C SW(Panel_WP)
CH1_CLK+	LVDS Channel 1, Clock +	43	I2C SW	Low : Switch Disable
				High : Switch Enable
GND	Ground	44	SDA	EEPROM Serial Data
				High(3.3V) for NS,
CH1_3-	LVDS Channel 1, Signal 3-	45	LVDS_SEL	Open/Low(GND) for JEIDA
CH1_3+	LVDS Channel 1, Signal 3+	46	N.C	No connection
				TCON Reset
CH1_4-	LVDS Channel 1, Signal 4-	47	ICON Reset	Low : Reset Disable, High : Enable
CH1 4+	LVDS Channel 1. Signal 4+	48	N.C	No connection
			WP	EEPROM WP
				No connection
	V _{DD} V _{DD} V _{DD} V _{DD} V _{DD} V _{DD} N.C. GND GND GND CH1_0- CH1_1- CH1_1+ CH1_2- CH1_2+ GND CH1_CLK- CH1_CLK- CH1_CLK- CH1_CLK-	VDDPower Supply, +12V DC RegulatedVDDPower Supply, +12V DC RegulatedVDDPower Supply, +12V DC RegulatedVDDPower Supply, +12V DC RegulatedVDDPower Supply, +12V DC RegulatedN.C.No connectionGNDGroundGNDGroundGNDGroundCH1_0-LVDS Channel 1, Signal 0-CH1_0+LVDS Channel 1, Signal 0-CH1_1-LVDS Channel 1, Signal 1-CH1_1-LVDS Channel 1, Signal 1-CH1_2-LVDS Channel 1, Signal 2-CH1_2+LVDS Channel 1, Signal 2-GNDGroundCH1_CLK-LVDS Channel 1, Clock -CH1_CLK-LVDS Channel 1, Clock -CH1_3-LVDS Channel 1, Signal 3-CH1_3-LVDS Channel 1, Signal 3-CH1_3+LVDS Channel 1, Signal 4-CH1_4-LVDS Channel 1, Signal 4-CH1_4-LVDS Channel 1, Signal 4-GNDGround	VDD Power Supply, +12V DC Regulated 26 VDD Power Supply, +12V DC Regulated 27 VDD Power Supply, +12V DC Regulated 28 VDD Power Supply, +12V DC Regulated 29 VDD Power Supply, +12V DC Regulated 30 N.C. No connection 31 GND Ground 32 GND Ground 34 CH1_0- LVDS Channel 1, Signal 0- 35 CH1_0- LVDS Channel 1, Signal 0- 36 CH1_1- LVDS Channel 1, Signal 1- 37 CH1_1- LVDS Channel 1, Signal 1- 38 CH1_2- LVDS Channel 1, Signal 2- 39 CH1_2- LVDS Channel 1, Signal 2- 40 GND Ground 41 CH1_CLK- LVDS Channel 1, Clock - 42 CH1_CLK- LVDS Channel 1, Clock - 42 CH1_3- LVDS Channel 1, Signal 3- 45 CH1_3- LVDS Channel 1, Signal 3- 45 CH1_4- LVDS Channel 1, Signal 4- 47	VDD Power Supply, +12V DC Regulated 26 CH3_0+ VDD Power Supply, +12V DC Regulated 27 CH3_1- VDD Power Supply, +12V DC Regulated 28 CH3_1+ VDD Power Supply, +12V DC Regulated 29 CH3_2- VDD Power Supply, +12V DC Regulated 30 CH3_2- N.C. No connection 31 GND GND Ground 32 CH3_2- GND Ground 32 CH3_CLK- GND Ground 34 GND GND Ground 34 GND CH1_0- LVDS Channel 1, Signal 0- 35 CH3_3- CH1_0- LVDS Channel 1, Signal 0- 36 CH3_3- CH1_1- LVDS Channel 1, Signal 1- 37 CH3_4- CH1_1- LVDS Channel 1, Signal 1- 38 CH3_4- CH1_2- LVDS Channel 1, Signal 2- 39 GND CH1_2- LVDS Channel 1, Clock - 42 N.C CH1_CLK- LVDS Channel 1, Sig





75

GND

T460HW08 V7 Product Specification Rev.0 1

PIN	Symbol	Description	PIN	Symbol	Description
51	N.C.	AUO Internal Use Only	76	CH2_2+	LVDS Channel 2, Signal 2+
52	GND	Ground	77	CH2_2-	LVDS Channel 2, Signal 2-
53	CH4_4+	LVDS Channel 4, Signal 4+	78	CH2_1+	LVDS Channel 2, Signal 1+
54	CH4_4-	LVDS Channel 4, Signal 4-	79	CH2_1-	LVDS Channel 2, Signal 1-
55	CH4_3+	LVDS Channel 4, Signal 3+	80	CH2_0+	LVDS Channel 2, Signal 0+
56	CH4_3-	LVDS Channel 4, Signal 3-	81	CH2_0-	LVDS Channel 2, Signal 0-
57	GND	Ground	82	GND	Ground
58	CH4_CLK+	LVDS Channel 4, Clock +			
59	CH4_CLK-	LVDS Channel 4, Clock -			
60	GND	Ground			
61	CH4_2+	LVDS Channel 4, Signal 2+			
62	CH4_2-	LVDS Channel 4, Signal 2-			
63	CH4_1+	LVDS Channel 4, Signal 1+			
64	CH4_1-	LVDS Channel 4, Signal 1-			
65	CH4_0+	LVDS Channel 4, Signal 0+			
66	CH4_0-	LVDS Channel 4, Signal 0-			
67	GND	Ground			
68	CH2_4+	LVDS Channel 2, Signal 4+			
69	CH2_4-	LVDS Channel 2, Signal 4-			
70	CH2_3+	LVDS Channel 2, Signal 3+			
71	CH2_3-	LVDS Channel 2, Signal 3-			
72	GND	Ground			
73	CH2_CLK+	LVDS Channel 2, Clock +			
74	CH2_CLK-	LVDS Channel 2, Clock -			
				-	

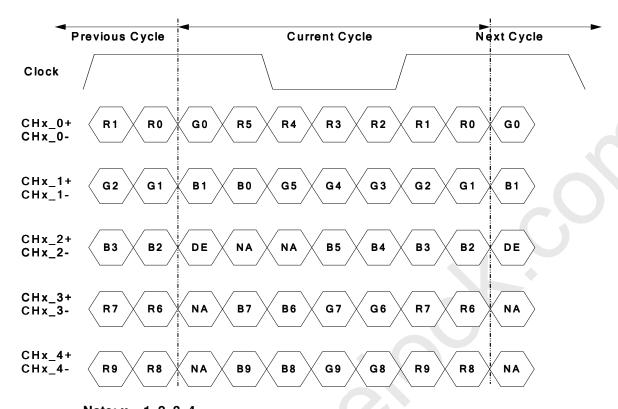
Note: N.C.: please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High).

Ground



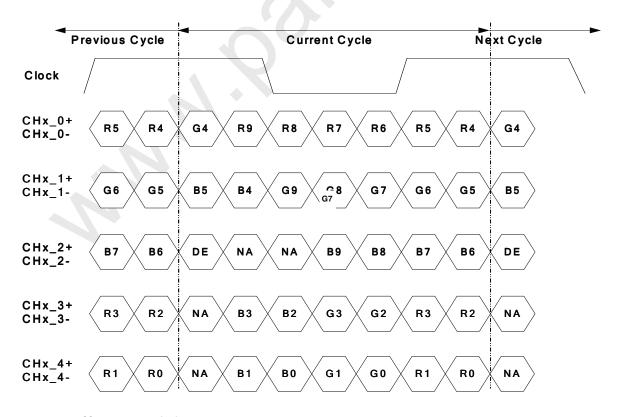
LVDS Option = High →NS

Global LCD Panel Exchange Center



Note: x = 1, 2, 3, 4...

LVDS Option = Low/Open →JEIDA



Note: x = 1, 2, 3, 4...



3.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table (DE only Mode)

Signal	Item	Symbol	Min.	Тур.	Max	Unit			
	Period	Tv	1090	1130	1392	Th			
Vertical Section	Active	Tdisp (v)		Th					
	Blanking	Tblk (v)	10	50	312	Th			
	Period	Th	540	570	580	Tclk			
Horizontal Section	Active	Tdisp (h)		480					
	Blanking	Tblk (h)	60	90	100	Tclk			
Clock	Frequency	Fclk=1/Tclk	64.8	77.29	80.74	MHz			
Vertical Frequency	Frequency	Fv	94	120	122	Hz			
Horizontal Frequency	Frequency	Fh	120	135.6	139.2	KHz			

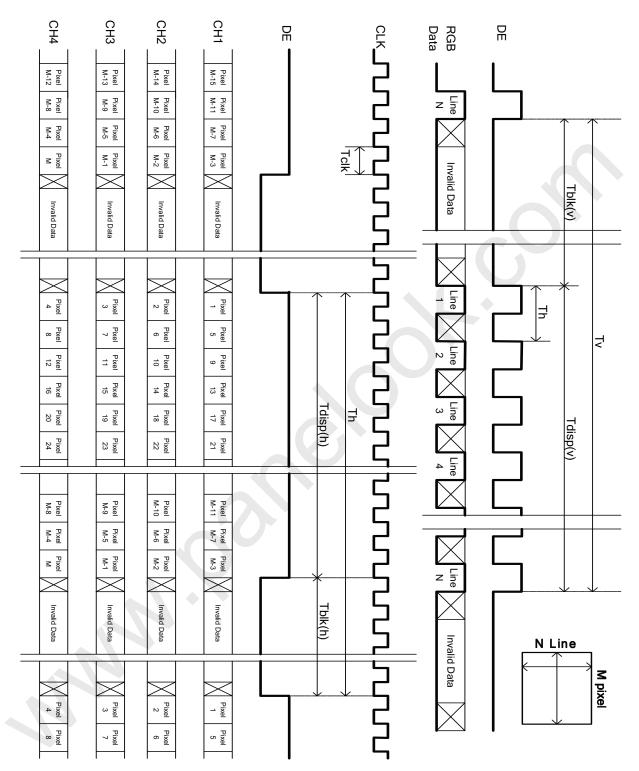
Notes:

- (1) Display position is specific by the rise of DE signal only.
 Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.
- (2) Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.
- (3)If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- (4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.





3.4 Signal Timing Waveforms







3.5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 10 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

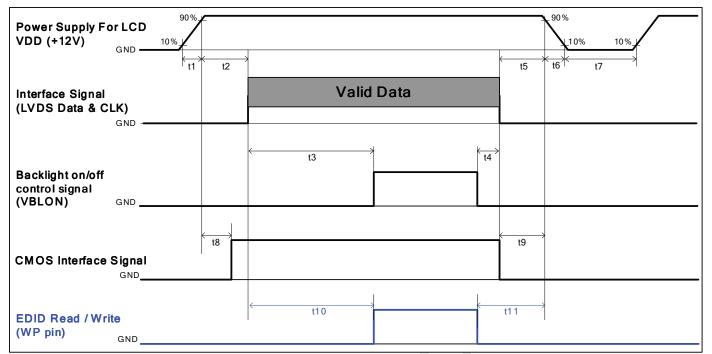
COLOR DATA REFERENCE

	COLOR DATA REFERENCE																							_							
		Input Color Data																													
	Color					RE	Ð									GRE	EEN	l				BLUE									
	00101	MSB							LSB M					MSB LSB						MSB LSB											
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	В9	B8	В7	В6	B5	В4	ВЗ	В2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	, 1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1 (1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R		,																													
	RED(1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
G				4																											
	GREEN(1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
В																															
	BLUE(1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1



3.6 Power Sequence for LCD

Global LCD Panel Exchange Center



Parameter	Min.	Min. Type.		Unit	
t1	0.4		30	ms	
t2	0.1		50	ms	
t3	450			ms	
t4	0 ^{*1}			ms	
t5	0			ms	
t6			*2 	ms	
t7	500			ms	
t8	10		50	ms	
t9	0			ms	
t10	450			ms	
t11	150 ^{*3}			ms	

Note:

- (1) t4=0 : concern for residual pattern before BLU turn off.
- (2) t6: voltage of VDD must decay smoothly after power-off. (customer system decide this value)
- (3) t11: the min value is decided by the download finish time of EDID 2Kbits.(when SCL over 30KHz)

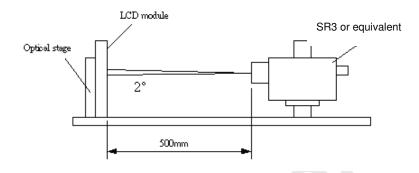




4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of φ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



D	O. mala al		Values			
Parameter	Symbol	Min.	Тур.	Max	Unit	Notes
Contrast Ratio	CR		6900*			1
Surface Luminance (White)	L _{WH}	(-)	335*		cd/m ²	2
Luminance Variation	δ _{WHITE(9P)}			1.33*		3
Response Time (G to G)	Тү		5.5		Ms	4
Color Gamut	NTSC		72*		%	
Color Coordinates						
Red	R _X		0.640*			
	R _Y		0.320*			
Green	G _X		0.300*	Typ.+0.03*		
	G _Y	T 0.00*	0.640*			
Blue	B _X	Typ0.03*	0.150*			
	B _Y		0.040*			
White	W _X		0.270*			
	W _Y		0.284*			
Viewing Angle						5
x axis, right(φ=0°)	θ_{r}		89		degree	
x axis, left(φ=180°)	θ_{l}		89		degree	
y axis, up(φ=90°)	θ_{u}		89		degree	
y axis, down (φ=270°)	$\theta_{\sf d}$		89		degree	





* LED lightbar and LED backlight structure are designed by the customer, AUO cannot guarantee the optical specification. The figures above are mean of 3pcs DVT samples measurements and are for reference only.

Note:

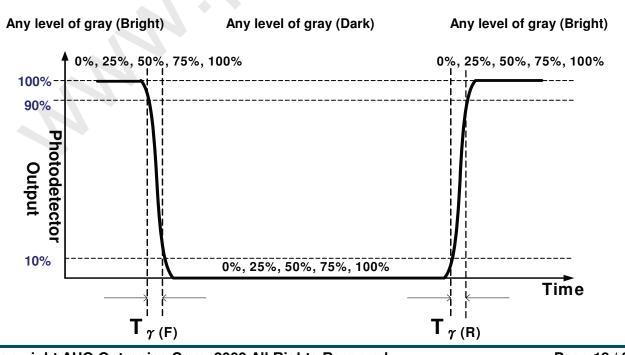
1. Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio=
$$\frac{\text{Surface Luminance of L}_{\text{on5}}}{\text{Surface Luminance of L}_{\text{off5}}}$$

- 2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When lamp current $I_H = 11 \text{mA}$. L_{WH} =Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.
- 3. The variation in surface luminance, δWHITE is defined (center of Screen) as:
 - $\delta_{WHITE(9P)} = Maximum(L_{on1},\,L_{on2},...,L_{on9})/\,\,Minimum(L_{on1},\,L_{on2},...\,L_{on9})$
- 4. Response time T_{γ} is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_{ν} =120Hz to optimize.

Measured		Target					
Response Time		0%	25%	50%	75%	100%	
	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%	
Start	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%	
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%	
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%	
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%		

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright)" and "any level of gray(dark)".



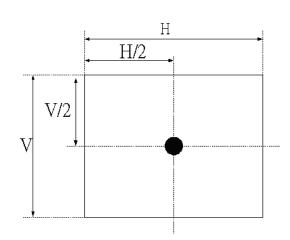
© Copyright AUO Optronics Corp. 2009 All Rights Reserved.

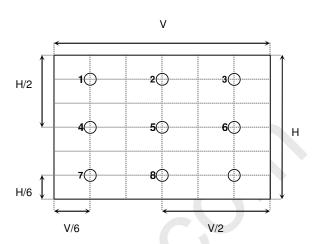


Global LCD Panel Exchange Center

T460HW08 V7 Product Specification Rev.0 1

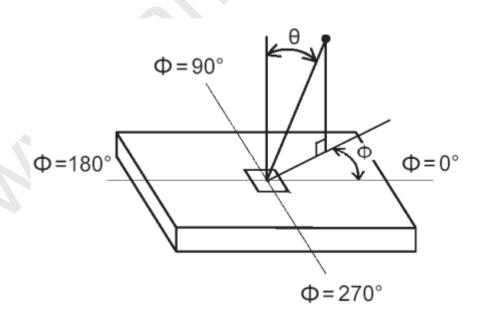
FIG. 2 Luminance





5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing Angle







5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T460HW08 V7. In addition the figures in the next page are detailed mechanical drawing of the LCD.

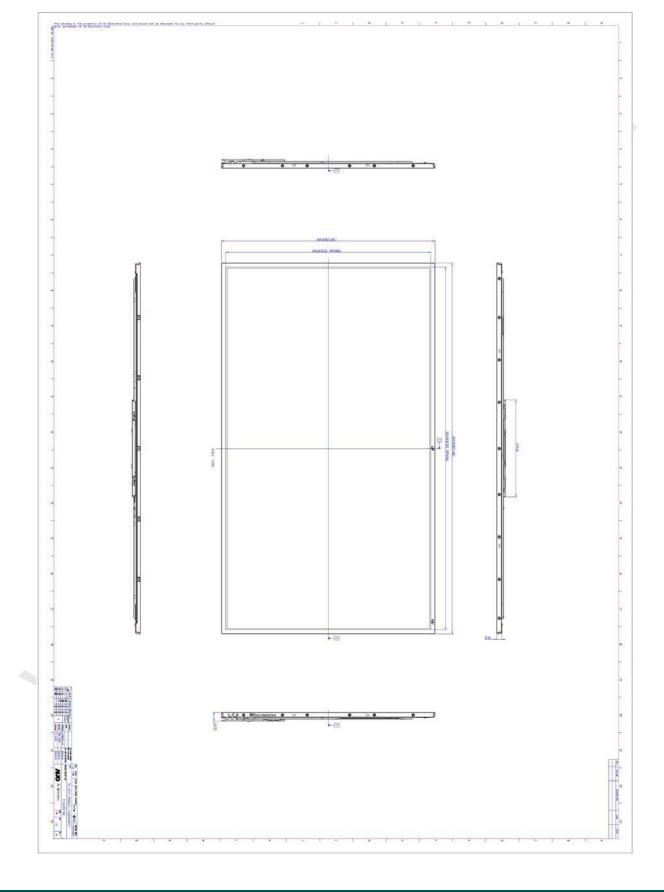
ltem		Dimension	Unit	Note	
Outline Dimension	Horizontal	1049.0	mm	Refer to customers' design	
	Vertical	604.6	mm	Refer to customers' design	
	Depth (Dmin)	14.9	mm	Refer to customers' design	
	Depth (Dmax)	24.7	mm	Refer to customers' design	
Weight	8600		g	Refer to customers' design	





Front View

* LED Backlight structure is designed by customer, AUO cannot guarantee this drawing

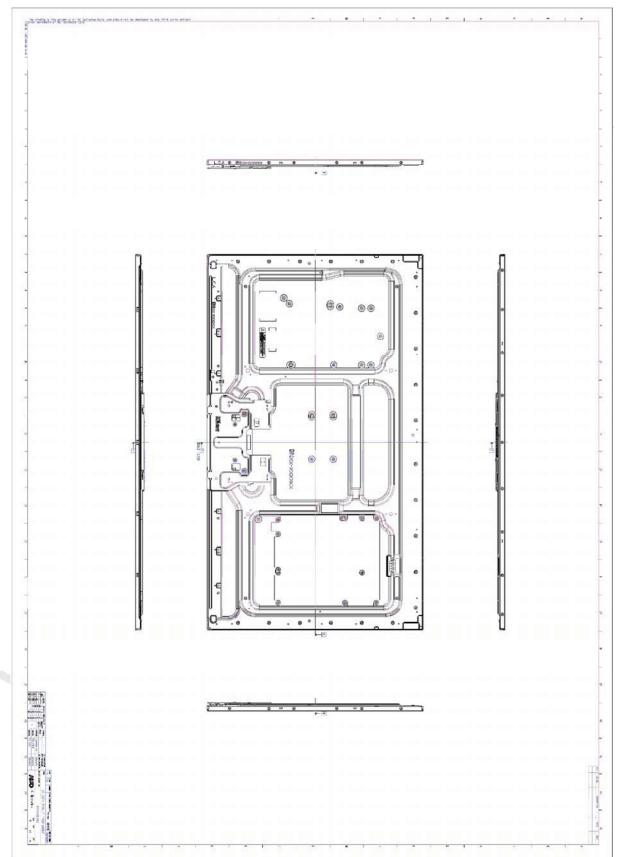






Back View

* LED Backlight structure is designed by customer, AUO cannot guarantee this drawing







6. Reliability Test Items

* The reliability test items are for reference only. AUO cannot guarantee all issue related to backlight and other customers' design.

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60°C, 300hrs
2	Low temperature storage test	3	-20℃ , 300hrs
3	High temperature operation test	3	50℃, 300hrs
4	Low temperature operation test	3	-5℃, 300hrs
			Wave form: random Vibration level : 1.0G RMS
5	Vibration test (non-operation)	3	Bandwidth : 10-300Hz
			Duration: X,Y,Z 10min per axes
			X,Y,Z: Horizontal, face up
6	Shock test (non-operation)	3	Shock level 50G, 11ms in ±X, ±Y axis, 35G, 11ms in ±Z axis Waveform: half sine wave Direction: One time each direction
7	Vibration test (With carton)	1 (PKG)	Random wave (1.05Grms 10~200Hz) Duration: X,Y,Z 10min per axes
8	Drop test (With carton)	1 (PKG)	Height:25.4cm, Surround four flats and bottom flat twice (ASTMD4169)





7. International Standard

7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1: 2001, IEC 60065:2001; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7.2 EMC

- (1) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998



Global LCD Panel Exchange Center

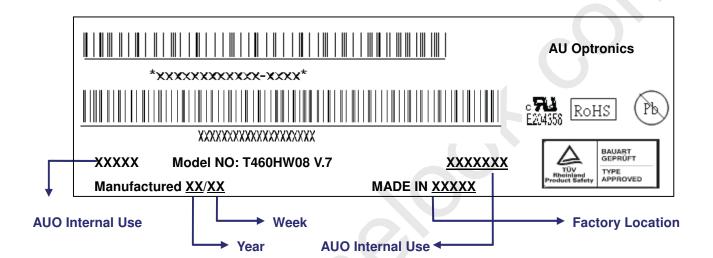
T460HW08 V7 Product Specification

8. Packing

8-1 DEFINITION OF LABEL:

A. Panel Label:





Green mark description

- (1) For Pb Free Product, AUO will add for identification.
- (2) For RoHs compatible products, AUO will add RoHS for identification.

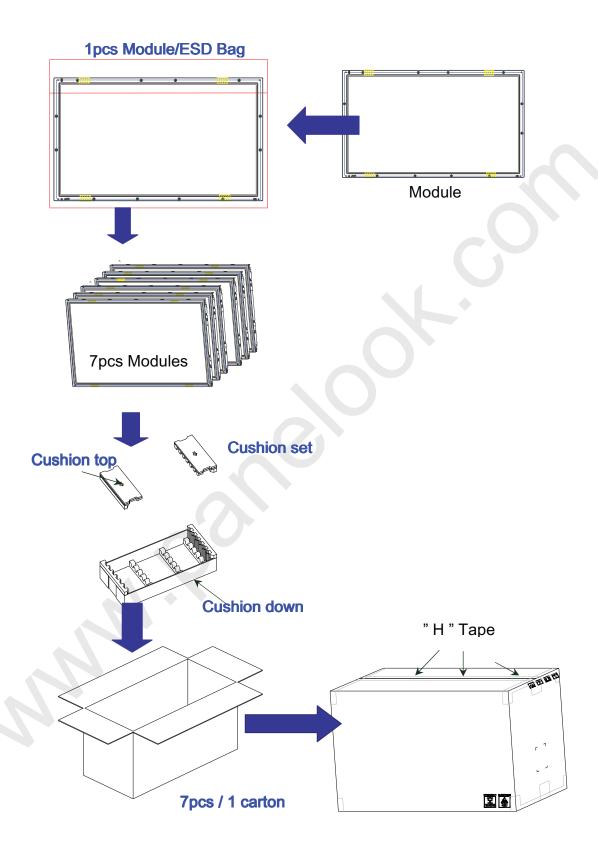
Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:





8-2 PACKING METHODS:

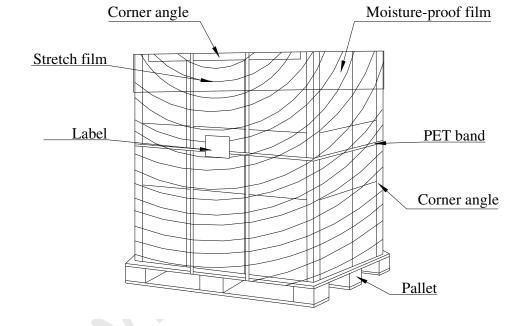






8-3 Pallet and Shipment Information

			Packing				
	Item	Qty.	Dimension Weight (kg)		Remark		
1	Packing Box	7pcs/box	pcs/box 1160(L)mm*375(W)mm*690(H)mm 60				
2	Pallet	1	1180(L)mm*1150(W)mm*132(H)mm 18				
3	Boxes per Pallet	3 boxes/Pall	3 boxes/Pallet (By Air) ; 3 Boxes/Pallet (By Sea)				
4	Panels per Pallet	21pcs/pallet(By Air); 21 pcs/Pallet (By Sea)					
5	Pallet 21(by A		1180(L)mm*1150(W)mm*822(H)mm (by Air)	199(by Air)			
	after packing	63(by Sea)	1180(L)mm*1150(W)mm*2466(H)mm (by Sea)	597 (by Sea)	40ft HQ		





9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer.

 Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of CCFL depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL





Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.